

Graphin to Announce Evaluation Suites for CMOS Sensors with MIPI Standard

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Graphin Co., Ltd. (President, Tomoaki KUROSAWA; Location, 20-5, Minami-ooi 3-chome, Shinagawa-ku, Tokyo 1400013 JAPAN), a rapidly emerging leader for evaluation suites for image sensors, announced today that it has developed the Advanced Board, an advanced evaluation product of CMOS Image Sensors (hereinafter called CIS) supported with the MIPI (Mobile Industry Processor Interface) standard and will ship them on a commercial basis no later than the end of 2008.

The CIS products for mobile devices are very rapidly evolved in pixels and motion pictures, and products with more than 10 mega pixels are expected to be available from manufacturers shortly. Transmission bandwidth in proportion to increasingly imaging data of the CIS is hitting G(Giga) bps as seen in high vision TV signals (1.485Gbps HD-SDI) and Gigabit Ethernet (1Gbps). A parallel interface, a legacy and global standard, has a limited capacity in high bandwidth for data communications, and is gradually shifted into a serial interface. The attention of the MIPI standard is attracted as a global standard of the next CIS for mobile devices.

Graphin has brought in vast experience of marketing the GPirates, evaluation suites for Image-Sensors to Image-sensor manufacturers and mobile device companies for CIS. Upon strong requests from markets and users, Camera Serial Interface (CSI-2) compliant with the MIPI standard is called for. Graphin has completed an evaluation for a D-PHY physical layer compliant with the MIPI standard and created a prototyping system with intellectual properties supporting CSI-2 protocol controls. Graphin is working on the Advanced Board commercially available to support these requirements.

About the GPirates and the Advanced Board from Graphin:

The GPirates are offered by Graphin, evaluation suites for image sensors. The Advanced Board is optionally available as an add-on board for GPLAB-1000U, GPLAB-1000P, and GPLAB-1000F from Graphin. The Advanced Board is embedded with the MIPI physical layers and controllers in FPGA. The Advanced Board will allow the GPirates series to support all the open standard for the CIS products.

Graphin will display and demonstrate the GPirates and the Advanced Board at the VISION Japan 2008 Graphin booths in Pacifico Yokohama scheduled for October 22nd through 24th, 2008.

***Features of the Advanced Board for the GPirates:**

:An add-on-board for GPLAB-1000U, GPLAB-1000P, and GPLAB-1000F from Graphin

:The MIPI (CSI-2) standard supported with 2 (two) lanes and speed up to 750Mbps

:Functionality to adjust skews in between lanes supported (possible configuration with 78p sec within FPGA)

:Image-Capture-Mode supported for a variety of image formats

:Packet-Capture-Mode supported to add a logger mode for serial packets

:The MIPI with 4 (four) lanes optionally available The GPirates commercially available:

:Market --- Research & development for image sensor products and cell phones, sales promotion with demos, and inspection testers at factories

:Pricing --- open pricing

:Timing --- shipping in early December 2008

About the MIPI Alliance:

The Mobile Industry Processor Interface (MIPI) Alliance is an open membership organization that includes leading companies in the mobile industry that share the objective of defining and promoting open specifications for interfaces in mobile terminals. For more information, please visit the MIPI Alliance at www.mipi.org.

About Graphin:

Graphin Co., Ltd., a subsidiary of TIETECH CO.,LTD. (JASDAQ:6893) was incorporated in January 1993 aiming to provide products that combine two different domains; the image processing technology of graphics and the information communication technology of interfaces. Graphin has interests in the domains of graphics and interface, many hardware products, OEM boards, and semiconductor intellectual properties (SIP) in Japan and has vast experience in the sales and support of various hardware and software. Their strengths include digital consumers, games, data compression algorithm, hardware design capability, FPGA and ASIC design, image processing algorithms, and knowledge of communications protocols. Graphin is a member of the MIPI Alliance and the SMIA (Standard Mobile Imaging Architecture) standard. For more information, please visit Graphin at www.g-in.co.jp or contact Graphin directly at phone +81-3-5493-1211 or email sales@g-in.co.jp.